



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fred D. Bailey et al.

Art Unit: 2815

Serial No.: 09/452,691

Examiner: J. A. Fenty

Filing Date: 12/02/1999

Docket No.: TI-27935

Customer No.: 23494

Conf. No.: 1638

Title: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER METAL INTERCONNECT LAYER

LETTER OF TRANSMITTAL

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner of Patents, P.O.Box 1450, Alexandria, VA 22313-1450.

5-6-05

Date

Marianna Smith
Marianna Smith

Enclosed please find the signed Supplemental Declarations in the above-identified application.

Please charge any necessary fee to Deposit Account No. 20-0668.

Respectfully submitted,

Jacqueline J. Garner
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